

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: M. MIYAZAKI, et al.
Application No.: Rule 1.53(b) Divisional of U.S. Patent Application Serial
No. 10/282,080, filed October 29, 2002
Filed: On even date herewith
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
Art Group of Parent: 2816
Examiner of Parent: T. T. Lam

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 17, 2003

Sir:

Prior to examination, please amend the above-identified application as listed
below and as set forth on the following pages:

Amendment to the Specification

Amendments to the Claims and

Remarks follow the amendments.